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Lothar Thiele, ETHZ, Switzerland  
Wang Yi, Uppsala University, Sweden

**Invited Speakers:**

TBA

**Important Dates:**

Abstract submission: 7 May 2012  
Paper submission: 11 May 2012  
Notification: 15 June 2012  
Final version due: 7 July 2012  
Conference: 18-20 September 2012

**Objectives and Scope of the Conference**

Timing aspects of systems from a variety of computer science domains have been treated independently by different communities. Researchers interested in semantics, verification and performance analysis study models such as timed automata and timed Petri nets. The digital design community focuses on propagation and switching delays while designers of embedded controllers have to take account of the time taken by controllers to compute their responses after sampling the environment. Timing related questions in these separate disciplines do have their particularities. However, there is a growing awareness that there are basic problems that are common to all of them. In particular, all these sub-disciplines treat systems whose behaviour depends upon combinations of logical and temporal constraints; namely, constraints on the temporal distances between occurrences of events. The aim of FORMATS is to promote the study of fundamental and practical aspects of timed systems, and to bring together researchers from different disciplines that share interests in modelling and analysis of timed systems. Typical topics include (but are not limited to):

- **Foundations and Semantics:** Theoretical foundations of timed systems and lan-guages; comparison between different models (timed automata, Petri nets, hybrid automata, max-plus algebra, probabilistic models).
- **Methods and Tools:** techniques, algorithms, data structures, and software tools for analyzing timed systems and resolving temporal constraints (scheduling, worst-case execution time analysis, optimisation, model-checking, testing, constraint solving, etc).
- **Applications:** adaptation and specialization of timing technology in application domains in which timing plays an important role (real-time software, hardware circuits, and problems of scheduling in manufacturing and telecommunication).

**Publication**

The proceedings of FORMATS 2012 will be published by Springer in the Lecture Notes in Computer Science series. Papers must contain original contributions, be clearly written, and include appropriate references to and comparison with related work. Simultaneous submission to other conferences with published proceedings is not allowed. Submissions should not exceed 15 pages, and should be formatted according to Springer LNCS guidelines. If necessary, the submission may be supplemented with a clearly marked appendix, which will be reviewed at the discretion of the program committee.

**About QEST 2012**

FORMATS 2012 will be co-located with QEST 2012 (http://www.qest.org/qest2012/) and simultaneous registration to both conferences will be possible.