scientific reports

Check for updates

OPEN In-situ strain control in epitaxial silicon carbide compound semiconductor

Behzad Jazizadeh & Maksym Myronov[⊠]

Residual strain in an epilayer grown on a foreign wafer induces epiwafer's bow, that is often considered undesirable. Wafer bow however, can be advantageous because both the direction and magnitude of strain are vital for the fabrication of various Micro Electro Mechanical Systems (MEMS), such as resonators. Here strain control is reported for highly mismatched heteroepitaxy of cubic silicon carbide (3C-SiC) compound semiconductor on silicon (Si), a prized functional material, dependent solely on carbon to silicon ratio (C/Si) during growth. While Si-rich condition enhances growth and generates positive curvature i.e. tensile strain, C-rich condition suppresses growth and produces negative curvature i.e. compressive strain. An optimum region emerges with virtually no strain and superior crystallinity. Our findings are significant for the knowledge of heteroepitaxy of 3C-SiC and may be broadened to heteroepitaxy of other compound semiconductors.

Keywords 3C-SiC, MEMS, CVD, Strain, Heteroepitaxy

Thin film cubic silicon carbide (3C–SiC) compound semiconductor is the only polytype of SiC that can be grown on a Si substrate. Technologically this implies fast, low cost and industrially scalable growth¹⁻⁸; but also means the generation of residual strain due to lattice mismatch⁹ and difference in the coefficients of thermal expansion¹⁰. As strain refers to the elastic distortion of crystal lattice under intrinsic stress gradient, strain-relaxation within the epilaver leads to unwanted defects and unintentional curvatures. While tremendous amount of research has been dedicated to the understanding and reduction of defects in 3C-SiC thin films on Si substrates over the years⁷⁻²² not much attention has been given to curvature (strain-stress) in the thin films. It is essential to note that while the presence of defects is harmful for applications in electronics, it is the curvature (bow) resulting from residual strain that is most devastating for epiwafers processing during microfabrication of electronics and other devices, but could be beneficial for Micro Electro Mechanical Systems (MEMS) applications.

Curvature in epiwafers (wafer bow), depending on severity, may lead to thickness nonuniformity in successively deposited layers, degradation in lithography accuracy, difficulty in handling the wafer by semiconductor manufacturing equipment with robotised wafer handling, and delamination followed by cracking of photoresist or deposited layers^{23,24}. Little work has been channelled towards managing or minimizing residual strain. Most important are the attempts to address the issue focusing on deposition parameters such as the choice of precursors, variation in temperature and controlling the deposition pressure^{25–27}. However, these efforts have resulted in degradation of crystalline quality, and there are serious doubts about their repeatability and possible effects on other material properties such as elemental composition. Most recently, there had been reignited strives to tackle the issue via introducing high levels of (aluminium) dopants into the epilayers^{28,29}, changing substrate orientation (from 001 to 111)30-32, modifying film thickness³² or post-growth enhancements via microstructures³³. These attempts though, are either short of complementary and independent investigation of the material system or require the substrate or the epilayer to be altered by additional growth steps or fabrication processing. Any solution to the problem at hand needs to be suitable for large-scale epitaxial wafer manufacturing as well as fast inexpensive processing. This article, for the first time, presents the strain adjustment of 3C-SiC thin films simply by way of tuning the carbon (C) content; here the C content refers to the C/Si atomic ratio between C-containing and Si-containing precursors in a gas phase during (one-step) growth³⁴ by chemical vapour deposition (see Methods, for more details). In-situ variation of C content has led to the transformation of strain in undoped 3C-SiC epilayers from tensile to zero to compressive, effectively taking in all possible states of residual strain.

An implication of this study is that: to overcome high levels of residual strain in heteroepitaxial layers it is not necessary to grow thicker films that in turn require longer growth times and larger amounts of precursors. In fact, thinner epilayer not only drives down the cost of growth, but also simplifies micromachining of MEMS and

Department of Physics, The University of Warwick, Coventry CV4 7AL, UK. 🖾 email: M.Myronov@warwick.ac.uk

NEMS structures. This is even more advantageous for a material such as silicon carbide that possesses mechanical and electrical properties that are superior to their counterparts, and for which thicker epilayer (longer growth) can thus be avoided. This however means that measurable wafer bow would be a remote possibility, as was the case in this study. As a result, the fabrication of microcantilevers provided the solution to measurability problem.

Presented in Fig. 1a an actual 3C–SiC/Si (001) epiwafer and the sample's schematic as it is cleaved along the $\langle 110 \rangle$ plane (parallel to primary flat of 100 mm diameter Si(001) wafer), respectively. All microcantilevers were patterned in order to be released with their lengths along the cleaving direction. Both curvature (left y–axis) and the deflection (right y–axis) profiles of the microcantilevers (Fig. 1b) are shown, as functions of *C* content, normalized to the thickness of epilayers: k_{norm} (referred to as *k*) and z_{norm} (referred to as *z*), respectively. Data points in the deflection plot refer to the maximum deflection, at the tip of the cantilever.

Datasets in this and other sections of the article were normalized to sample thickness by subtracting the mean thickness value from each individual thickness data. This difference was then divided by the standard deviation, as given below:

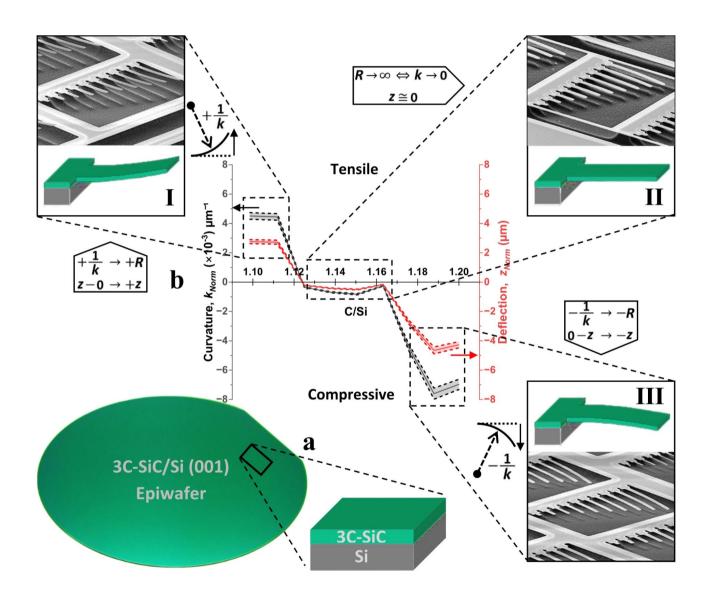


Fig. 1. Schematics of 3C–SiC/Si (001) epiwafer, sample and cantilevers, and elastic measurements. (**a**) Optical photograph of an actual 3C–SiC/Si (001) epiwafer, one of the series with different *C* contents grown in house for this study, from which a standard sample is cleaved parallel to the large wafer flat. (**b**) A superposition plot of *k* (left Y–axis) and *z* (right Y–axis) as a function of *C* content (shown as *C/Si*). **I**, **II** and **III** Insets show cantilever schematics along with SEM images of cantilever arrays with state of curvature in tensile (thickness 329 nm), neutral (thickness 302 nm) and compressive (thickness 273 nm), respectively. Dashed lines indicate the 5% error margin.

Scientific Reports | (2024) 14:30325

$$X := \frac{X - m}{s} \tag{1}$$

where X are data values, m is the mean of the data and s is calculated as:

$$s = \sqrt{E[X^2] - (E[X])^2}$$
 (2)

where $E[X^2]$ is the mean of the squared data and $(E[X])^2$ is the square of the mean of data (m = E[X]). This process generated normalized (dimensionless) values corresponding to each thickness value that were utilized as coefficients for further calculations where normalization deemed to be needed.

Curvature and deflection reach the highest positive values at lesser *C* content and drop to the lowest negative values at greater *C* content exhibiting large tensile and compressive strains, respectively. This is also supported by the scanning electron microscopy (SEM) of the arrays of microcantilever with different lengths, for positive and negative curvature, presented by insets I and III of Fig. 1b, respectively. A transition region of minimal strain exists, with C/Si values mid–way between tensile and compressive in Fig. 1b. A closer look at this region reveals that, even in a region of virtually strain–free epilayers, there exists some residual strain that is of a compressive nature. A further look at Fig. 1b inset II shows that while almost all cantilevers look straight, the longest one undergoes slight compression which suggests the presence of a critical aspect ratio for strain–free beams. Note that sign determination refers to the deflections away from substrate as positive or + z, and towards the substrate as negative or -z; as schematically visualized next to SEM images.

Reduction in thickness of the epilayers as a function of increasing *C* content is demonstrated in Fig. 2a. In Fig. 2b the left (vertical) axis represents full width at half maximum (*FWHM*) values that were obtained from rocking curve (RC) x-ray diffractometry (XRD) measurements of (002) plane and normalized to the thickness of the epilayers; while the right axis shows *R* values normalized to the thickness. Additional *C* content beyond $C/Si \approx 1.165$ affects both crystalline and mechanical properties corresponding to increase in both *FWHM* and *R* values. Insets I and II of Fig. 2b clearly visualize the (002) RC curve peak shift to the left and right from the relaxed position, for the tensile and compressive *R*, respectively, matching the Si–rich and C–rich growth regimes. Presented in Fig. 2c, for comparative purposes, is a characteristic high-resolution HR–XRD coupled scan of the Si–rich, C–rich, and optimum region, as previously specified in Fig. 2a, b. The substrate Bragg peaks, Si (004), is kept for reference and others from multiple crystal planes, where the Bragg peaks would have been positioned, are marked for clarity.

Note the optimum range that exists following the switch in signs of both curvature and deflection from tensile to compressive and then the sharp drop into more compressive values. Depicted by inset II of Fig. 1b this optimum region (or optimum range) with *C* contents within the interval of $1.125 \le C/Si \le 1.165$ was observed, according to the SEM imaging, to have very little to no deflection, and is identified as an interval with the curvature and deflection values being an order of magnitude smaller compared to the rest of the profile. This showcases a region of virtually no-bending for 3C–SiC epilayers. The deflection and curvature profiles embolden similarities with the variation in epilayers' thickness, seen in Fig. 2a.

As all epiwafers have similar growth time, the drop in thickness of 3C–SiC means reduced growth rate, as a result of increased *C* content. When the precursors decompose near the growth surface (Si substrate), methyl (CH₃) groups are introduced into surface reactions; they are relatively stable transient intermediates and remain mobile across the growth surface. For the duration of their stay on the growth surface, CH₃ groups effectively block Si atoms from bonding to C atoms, see Fig. 3a. These CH₃ groups form methane (CH₄) or ethane (C₂H₆) upon recombination with H atoms or neighbouring CH₃ groups, respectively (see Fig. 3b); and eventually leave the surface (see Fig. 3c). It is speculated that the excess C atoms (blocked from bonding with Si atoms) will be incorporated into the lattice structure, as apparent from Fig. 2a.

The curvature-deflection behaviour of the epilayers, and in particular the transition from tensile to compressive strain, is comparable with the overall trend in *FWHM* values (Fig. 2b left vertical axis). The largest *FWHM* values (of Si-rich region) correlate with the largest tension (the most positive values), while the rise of the *FWHM* values within the C-rich agree with the largest compression (the most negative values). In the same way, the *FWHM* values of the optimum region (implying the highest crystal quality of all samples) are in fact positioned where the minimally bent 3C–SiC epilayers show only slight compression that are (on average) an order of magnitude lower than both the positive and negative peaks. All this is while only the 3C–SiC (002) and (004) Bragg peaks were observable, across all samples; and the absence of Bragg peaks associated with planes other than those equivalent to 3C–SiC crystal structure implies that the 3C–SiC epilayers is monocrystalline, regardless of the evolving *FWHM* values as a function of *C* content.

Here we described the state of curvature–deflection of 3C–SiC/Si epilayers, representing the residual strain, as controlled only using C/Si (or rather the C content) during growth. Reciprocal space mapping (RSM) of the (004) and (224) planes, as shown in Fig. 4a, are a confirmation that while the epilayers with non-optimum C/Si ratio (see Fig. 4a, right hand side for C/Si \approx 1.2) are strained, they are free of tilt (see Fig. 4a, left hand side symmetrical scan). The intensity distribution of the asymmetrical 3C-SiC (224) scan visualizes the compressive shift, away from the fully relaxed line (delineated with the diagonal dashed line); which is in agreement with the curvature and deflection measurements (Fig. 2b, inset II) and the SEM images of the cantilever profiles (Fig. 1b, inset III). The evolution of the state of strain in 3C-SiC/Si(001) as a function of C content is hypothesized to be the result of structural defects that are generated within the epilayers during growth. These defects whose impact is enough to deeply transform mechanical properties, but not sufficient to fundamentally change the

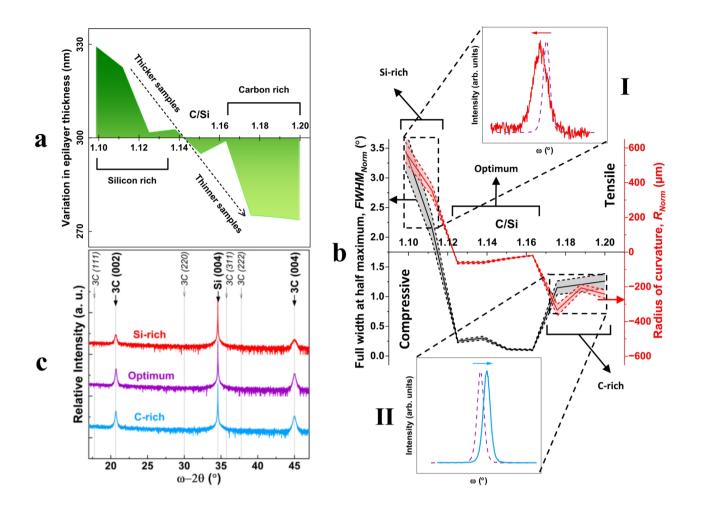


Fig. 2. Thickness, curvature variation and crystal quality. (a) Thickness measurements as a function of *C* content (shown as *C/Si*) denoting thicker at the Si–rich and thinner at the C–rich end. (b) A superposition plot of (002) RC FWHM (left Y–axis) and R (right Y–axis), as a function of *C* content (shown as *C/Si*). **I**, Inset showing a typical RC curve at the Si–rich end, shifted to lower angles indicating uniform tension, and **II**, Inset showing a typical RC curve at the C–rich end, shifted to higher angles indicating uniform compression. Both down–shift and up–shift of RC angles are referenced to the relaxed RC curve shown as dashed lines in insets **I** and **II**. (c) Plot of a representative XRD spectra of the 3C–SiC/Si (001), for the Si–rich, optimum region and C–rich offset at the top, middle and bottom of the plot, respectively. Available Bragg peaks for 3C (002), Si (004) and 3C (004) are marked by bold large fonts with thick arrows, while Bragg peaks that are unavailable (noted above the XRD spectra) are marked by italic small fonts with thin arrows.

monocrystalline characteristics of the epilayers, seem to alter the three regions Si–rich, optimum and C–rich differently. As C content increases it is postulated that these structural defects, in the C–rich, are attributed to the C content that is integrated into the crystal lattice, the nature of which is still under investigation. Of all the possible types of crystallographic defects due to excess C, two main arrangements seemed to the authors as the most probable ones. Those are schematically presented in Fig. 4b and c, in the forms of substitutional C and interstitial C, respectively; suggesting the possibilities of having either shallow or deep electronic states within the bandgap. Further studies are underway to obtain a deeper understanding of the exact types and natures of the defects. To the best of our knowledge, this is the first time that a material has been developed with strain properties tuneable in such a way that all three states of tensile, zero, and compressive are accessible. Results and discussions in this study can be further extended to the heteroepitaxial growth of other technologically important compound semiconductors, both current and emerging, such as the likes of gallium nitride (GaN) and aluminium nitride (AlN), as well as boron arsenide (BAs) and boron nitride (BN), respectively.

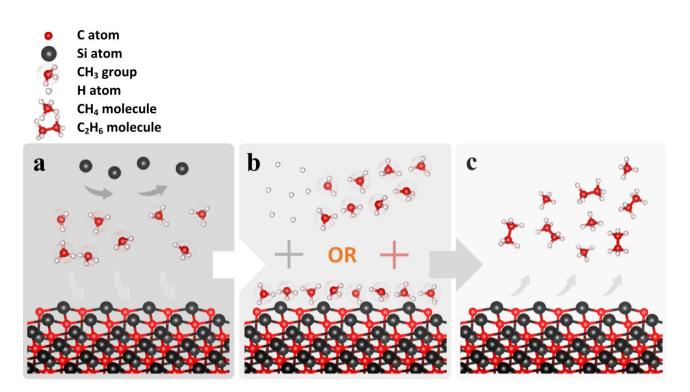


Fig. 3. The role of CH₃ groups in the C–rich growth region. (a) stable CH₃ groups arrive at the surface and block Si atoms. (b) CH₃ groups are then joined by either H atoms or other CH₃ groups nearby. (c) CH₃ groups, following near–surface reactions, leave either in the form of CH₄ or C₂H₆ molecules.

Methods Growth of epilayers

High quality 3C–SiC epilayers, variable in thickness, have been grown at low temperatures using a single-step epitaxy in an industry standard Si based reduced pressure chemical vapor deposition (RP–CVD) system. Growth was carried out on on-axis 100 mm diameter 525 μ m thick Si(001) wafers within an ASM Epsilon 2000 cold wall system, capable of growth on wafers of up to 200 mm diameter. Growth rates of over 10 μ m/hr was obtained. The Si wafers were loaded into the CVD growth chamber at 900 °C. The temperature was then rapidly raised to ~1000 °C to thermally desorb the native oxide from Si surface. The 3C–SiC growth immediately followed at below 500 Torr chamber pressure, and with no carbonization steps involved. More details about the growth can be found in reference³⁴. A mixture of Trimethylsilane (TMS) and Dichlorosilane (DCS) was used as the growth precursors. To realize optimal growth conditions and control the 1:1 stoichiometry of 3C–SiC, the following crucial formulation was calibrated and maintained:

$$\frac{C}{Si} = \frac{3 \times TMS}{DCS + TMS} \tag{3}$$

From which the variation of C/Si was achieved by carefully adjusting the TMS flow ratio.

Fabrication of cantilevers

Arrays of 3C–SiC test devices (microcantilevers) with variable aspect ratios were suspended from Si substrate, and examined in terms of their mechanical parameters. The fabrication process for all samples was performed in parallel and at the same time, with identical steps and parameters; this eliminated any possible differences that could appear from one sample to another, among other factors, as a result of difference in the duration of exposure to outside contaminants. As shown in Fig. 5 the fabrication process started with a low temperature silicon dioxide (SiO₂) layer of similar thickness to the carbide (3C–SiC) layer, deposited to be used as etch mask. Samples were then patterned with the layout of the microdevices using direct write lithography exposing a thin layer of photoresist (resist), followed by resist development. Device patterns were then transferred onto the oxide layer inside the reactive ion etcher using a 200 *W* RF power Trifluoromethane / Argon (CHF₃ / Ar) plasma, with 17.7 / 20.1 standard cubic centimeter per minute (*sccm*) flow respectively. The final pattern transfer and release of the devices, took place inside a inductively coupled plasma (ICP) deep reactive ion etcher. Etch parameters were initially set to 2000 *W* (source RF), 100 *W* (electrode RF) to generate a Sulfur hexafluoride / Oxygen (SF₆ / O₂) plasma, with 55 / 10 *sccm* for 3C–SiC, and then finally set to 2000 *W* (source RF) with electrode RF source turned down to zero to strike a SF₆-only plasma for Si; thereby developing an undercut to suspend the devices. Resonators were designed as cantilever beams (single clamped); with one end of the beam fixed (anchored) and

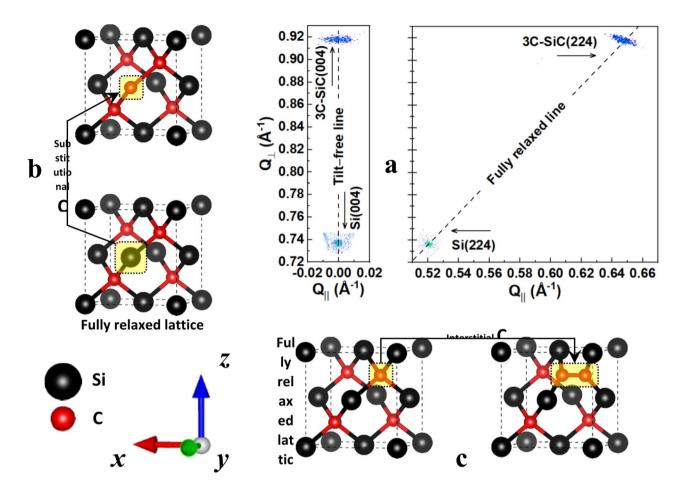


Fig. 4. Reciprocal space map, substitutional and interstitial defects. (a) Reciprocal space map (RSM) of a typical strained epitaxial 3C–SiC (here at $C/Si \approx 1.2$); with the (004) and (224) maps showing the tilt and strain, respectively. Dashed lines represent the reference positions for the tilt–free and strain–free (fully relaxed) epilayers, respectively. (b) Schematic showing the possibility of substitutional C incorporation, as C/Si ratio increases. (c) Schematic showing the possibility of interstitial C incorporation, as C/Si ratio increases.

one end free to move. The microcantilevers were patterned in 1D arrays with fixed width (5 μ m) and variable lengths (7 μ m to 98 μ m).

Characterization

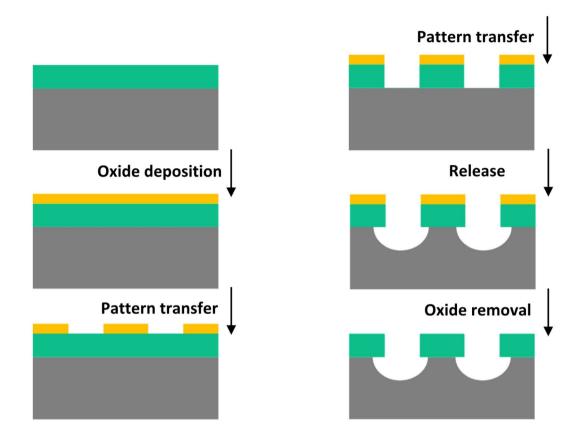
Structural properties of the 3C–SiC crystals were analysed using X–ray diffractometer, including X–ray diffraction (XRD), rocking curves' analysis (RC) and reciprocal space mapping (RSM) analysis all on PANalytical X'Pert Pro MRD. Images were obtained from the Zeiss SUPRA 55VP FEGSEM scanning electron microscope (SEM). Thickness of epilayers was measured by spectroscopic ellipsometry (SE), on Accurion Nanofilm EP4, and confirmed by Fourier transform infrared (FTIR) on Bruker Vertex 70v, and X–ray reflectivity (XRR). Deflections of the microcantilevers were measured using white light interferometry.

Tip deflections, as the maximum cantilever deflection, were utilized in the deflection plot. The data points for curvature values were taken from a triplet set of deflection measurements, separately spaced along the length of the cantilever. Cantilevers with similar lengths and from the same position were chosen from each array. Five arrays of cantilevers were randomly chosen for five measurements; and the final deflection value was taken as the average of those measurements. Curvature (k) was then approximated using the Menger definition and method^{35,36}, as:

$$k = \frac{1}{R} \tag{4}$$

And R is the radius of the circle that passes through the triplet of data points, and is obtained using:

$$R = \frac{abc}{4S} \tag{5}$$



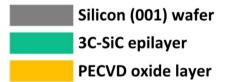


Fig. 5. Cantilever fabrication process. All–dry steps low temperature fabrication process for microcantilevers leading to stiction–free highly etch-selective suspended structures.

.....

where *a*, *b* and *c* are respectively the side lengths corresponding to vertices *A*, *B* and *C* (the data points); and *S* is the surface area of the triangle created by the three sides *a*, *b* and *c*. A more detailed formulation is given in reference³⁵. Measurement error from one sample cantilever to the next fell within one standard deviation.

Data availability

All data are available in the main text and supplementary materials. Any additional data are available from the corresponding author upon request.

Received: 18 June 2024; Accepted: 21 November 2024 Published online: 05 December 2024

References

- 1. Casady, J. B. Processing of Silicon Carbide for Devices and circuits. In: (ed Pearson, S. J.) Processing of Wide Band Gap Semiconductors. William Andrew Publishing: New York, U. S. A., 178–249. (2000).
- 2. Properties of Silicon Carbide INSPEC, the Institution of Electrical Engineers: London, U. K., (1995).
- 3. Morkoç, H. et al. Large-band-gap SiC, III-V nitride, and II-VI ZnSe-based semiconductor device technologies. J. Appl. Phys. 76, 1363–1398 (1994).
- 4. Casady, J. B. & Johnson, R. W. Status of silicon carbide (SiC) as a wide-bandgap semiconductor for high-temperature applications: a review. *Solid-State Electron.* **39**, 1409–1422 (1996).

- 5. Advances in SiC Processing and Applications. Artech House, Inc.: Boston, U. S. A., (2004).
- 6. Severino, A. et al. La Via, 3 C-SiC film growth on Si substrates. ECS Trans. 35, 99-116 (2011).
- Wellmann, P. et al. F. La Via, Status of 3 C-SiC Growth and Device Technology. In: (eds Wellmann, P., Ohtani, N. & Rupp, R.) Wide Bandgap Semiconductors for Power Electronics, vol. 2. WILEY-VCH GmbH: Weinheim, Germany 711. (2022).
- Silicon Carbide *Epitaxy*. Research Signpost: Kerala, India, (2012).
 Long, C., Ustin, S. A. & Ho, W. Structural defects in 3 C–SiC grown on Si by supersonic jet epitaxy. *J. Appl. Phys.* 86, 2509–2515 (1999).
- 10. Slack, G. A. & Bartram, S. F. Thermal expansion of some diamondlike crystals. J. Appl. Phys. 46, 89-98 (1975).
- 11. Masri, P. et al. The physics of heteroepitaxy of 3 C–SiC on Si: role of Ge in the optimization of the 3 C–SiC/Si heterointerface. *Mater. Sci. Eng. B.* **61–62**, 535–538 (1999).
- Sarney, W. L., Wood, M. C., Salamanca-Riba, L., Zhou, P. & Spencer, M. Role of Ge on film quality of SiC grown on Si. J. Appl. Phys. 91, 668–671 (2002).
- Piluso, N. et al. Analysis on 3 C-SiC layer grown on Pseudomorphic-Si/Si_{1-x}Ge_x/Si(001) Heterostructures. *Mater. Sci. Forum.* 806, 21–25 (2014).
- 14. Nagasawa, H., Yagi, K., Kawahara, T. & Hatta, N. Reducing planar defects in 3 C-SiC. Chem. Vap. Deposition. 12, 502-508 (2006).
- 15. Nagasawa, H. et al. High quality 3 C-SiC substrate for MOSFET Fabrication. Mater. Sci. Forum. 711, 91-98 (2012).
- 16. La Via, F. et al. Patterned substrate with inverted silicon pyramids for 3 C-SiC epitaxial growth: A comparison with conventional (001) Si substrate. J. Mater. Res. 28, 94–103 (2012).
- von Känel, H. et al. Three-dimensional epitaxial Si_{1-x}Ge_x, Ge and SiC crystals on deeply patterned Si substrate. ECS Trans. 64, 631–648 (2014).
- 18. Polychroniadis, E. K. et al. Comparative evaluation of free-standing 3 C-SiC crystals. Mater. Sci. Forum. 483-485, 229-232 (2005).
- 19. Nagasawa, H., Kawahara, T., Yagi, K. & Hatta, N. Propagation of stacking faults in 3 C-SiC. Mater. Sci. Forum. 679-680, 282-285 (2011).
- Yagi, K., Kawahara, T., Hatta, N. & Nagasawa, H. Switch-back Epitaxy' as a novel technique for reducing stacking faults in 3 C-SiC. Mater. Sci. Forum. 527–529, 291–294 (2006).
- 21. Sakwe, S. A., Müller, R. & Wellmann, P. J. Optimization of KOH etching parameters for quantitative defect recognition in n- and p-type doped SiC. J. Cryst. Growth. 289, 520–526 (2006).
- Sakwe, S. A., Müller, R. & Wellmann, P. J. Erratum to optimization of KOH etching parameters for quantitative defect recognition in n- and p-type doped SiC. J. Cryst. Growth. 299, 1 (2007).
- 23. Kai, Y. Wafer warpage detection during bake process in photolithography. Master of Engineering thesis, National University of Singapore, Singapore, (2003).
- Kim, Y., Kang, S. K., Kim, S. D. & Kim, S. E. Wafer warpage analysis of stacked wafers for 3D integration. *Microelectron. Eng.* 89, 46–49 (2012).
- 25. Bustarret, E. et al. Interfacial strain in 3 C-SiC/Si(100) pseudo-substrates for cubic nitride epitaxy. *Phys. Status Solidi (a).* 195, 18–25 (2003).
- 26. Zielinski, M. et al. Strain and wafer curvature of 3 C-SiC films on silicon: Influence of the growth conditions. *Phys. Status Solidi* (a). 204, 981–986 (2007).
- 27. Severino, A. et al. High-quality 6inch (111) 3 C-SiC films grown on off-axis (111) Si substrates. Thin Solid Films. 518, S165–S169 (2010).
- 28. Michaud, J. F. et al. Influence of aluminum incorporation on mechanical properties of 3 C–SiC epilayers. *Mater. Sci. Forum.* **924**, 318 (2018).
- 29. Ben Messaoud, J., Michaud, J. F., Zielinski, M. & Alquier, D. Effects of aluminum incorporation on the Young's modulus of 3 C-SiC epilayers. *Mater. Sci. Forum.* **963**, 305 (2019).
- 30. Michaud, J. F. et al. Micromachining of thin 3 C-SiC films for mechanical properties investigation. *Materials Research Society Symposium Proceedings 1246*, 213 (2010).
- Michaud, J. F., Portail, M. & Alquier, D. 3 C-SiC: from electronic to MEMS devices in Advanced Silicon Carbide Devices and Processing, Edited by Stephen E. Saddow and Francesco La Via, InTech, ISBN 978-953-51-2168-8 (2015).
- 32. Atieh Ranjbar Kermany. Francesca Iacopi; Controlling the intrinsic bending of hetero-epitaxial silicon carbide micro-cantilevers. J. Appl. Phys. 21 (15), 155304 (October 2015).
- Nguyen, H. P. P. T. K. et al. Ryutaro Maeda, Dzung Viet Dao, Nam-Trung Nguyen; Ultra-high strain in epitaxial silicon carbide nanostructures utilizing residual stress amplification. *Appl. Phys. Lett. 3 April.* 110 (14), 141906. https://doi.org/10.1063/1.4979834 (2017).
- 34. Myronov, M. & Colston, G. Single step silicon carbide heteroepitaxy on a silicon wafer at reduced temperature. *Mater. Today Commun.* **38**, 108312 (2024).
- 35. Léger, J. C. Menger curvature and Rectifiability. Ann. Math. 149, 831-869 (1999).
- 36. Johnson, R. A. Modern Geometry; an Elementary Treatise on the Geometry of the Triangle and the Circle, Edited by John Wesley Young. Boston; Houghton Mifflin Company, Print. (1929).

Acknowledgements

We thank Dr Alan Burton for technical support of equipment and clean room facilities.

Author contributions

M. M. designed the experiment, developed the CVD growth technology and grew the 3C–SiC/Si epi wafers, B. J. fabricated the cantilevers and characterised the materials, M. M. and B. J. analysed the data and wrote the article.

Declarations

Competing interests

The authors declare no competing interests.

Additional information

Correspondence and requests for materials should be addressed to M.M.

Reprints and permissions information is available at www.nature.com/reprints.

Publisher's note Springer Nature remains neutral with regard to jurisdictional claims in published maps and institutional affiliations.

Open Access This article is licensed under a Creative Commons Attribution 4.0 International License, which permits use, sharing, adaptation, distribution and reproduction in any medium or format, as long as you give appropriate credit to the original author(s) and the source, provide a link to the Creative Commons licence, and indicate if changes were made. The images or other third party material in this article are included in the article's Creative Commons licence, unless indicated otherwise in a credit line to the material. If material is not included in the article's Creative Commons licence and your intended use is not permitted by statutory regulation or exceeds the permitted use, you will need to obtain permission directly from the copyright holder. To view a copy of this licence, visit http://creativecommons.org/licenses/by/4.0/.

© The Author(s) 2024